

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Christophe Chevallier  
 Attorney Docket No.: 400.069US09  
 Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES

Commissioner for Patents  
 Mail Stop: Patent Application  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

16834 U.S. PTO  
 10/784508  
 022304

We are transmitting the following documents along with this Transmittal Sheet:

☒ Incorporate by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*

☒ **DIVISIONAL** of prior Patent Application No. 10/017,664 filed December 12, 2001 under 37 CFR 1.53(b) comprising:

- ☒ Specification 20 pgs., including claims numbered 1-15 and a 1 page abstract;
- ☒ 8 Sheets of Formal drawings;
- ☒ Communication Regarding Divisional Application (2 pgs.);
- ☒ A copy of the Declaration from prior application (3 pgs.);
- ☒ A copy of Power of Attorney from prior application (1 pg.);
- ☒ Prior application is assigned of record to: Micron Technology, Inc.;
- ☒ Information Disclosure Statement (1 pg.) and Form 1449 (1 pg.) (References not enclosed, cited in prior application);
- ☒ A check in the amount of \$770.00 to cover the Large Entity filing fee, which is calculated below:

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 DEPOSIT ACCOUNT 501373**

**CUSTOMER NUMBER 27073**

| APPLICATION FILING FEE  |                                     |   |                                    |                         |              |
|---|-------------------------------------|---|------------------------------------|-------------------------|--------------|
|   | Number of<br>Claims<br>Filed<br>(1) | Claims Included in<br>Basic Filing Fee<br>(2) | Number of<br>Extra Claims<br>(1-2) | Cost per Extra<br>Claim | Fee Required |
| Total Claims  | 15                                  | - 20 =  | 0                                  | x \$18.00 =             | \$ 0.00      |
| Independent Claims  | 2                                   | - 3 =   | 0                                  | x \$86.00 =             | \$ 0.00      |
| One or More Multiple Dependent Claims Presented? If Yes, Enter \$280 Here * |                                     |   |                                    |                         | \$           |
| Enter Basic Filing Fee (Utility Patent-\$770.00) Here *                     |                                     |   |                                    |                         | \$ 770.00    |
| Total   |                                     |   |                                    |                         |              |
| Total Application Filing Fee  |                                     |   |                                    |                         | \$770.00     |

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**Express Mail" mailing label number: EV372993423US Date of Deposit: February 23, 2004**

These papers and fees are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and addressed to Commissioner for Patents, Mail Stop: Patent Application, P. O. Box 1450, Alexandria, VA 22313-1450.

(LARGE ENTITY TRANSMITTAL UNDER 37 C.F.R. 1.10)

|   |                       |  |
|---|-----------------------|--|
| First Named Inventor  | Christophe Chevallier | <b><u>COMMUNICATION</u></b><br><b><u>REGARDING DIVISIONAL</u></b><br><b><u>APPLICATION</u></b> |
| Serial No.  | Unknown               |  |
| Filing Date   | Herewith              |  |
| Group Art Unit  | Unknown               |  |
| Examiner Name   | Unknown               |  |
| Attorney Docket No.   | 400.069US09           |  |
| Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES |                       |  |

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Prior to taking the above-entitled patent application under examination, please review the following comments:

#### **COMMENTS**

This is a divisional application of U.S. Patent Application Serial No. 10/017,664 (the '664 application), titled "FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES," filed December 12, 2001 (pending). The '664 application was subjected to a Restriction Requirement by Examiner Viet Q. Nguyen in the Restriction Requirement mailed December 31, 2003. Applicant contends that the claims of the present divisional application comport with the subject matter of the non-elected claim 47 of the '664 application.

The specification has been amended to add a cross reference to the '664 application, to conform the Summary to the claims, and to update references to cited patent applications. Formal drawings are submitted.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at 612 312-2200.

Date: \_\_\_\_\_

2/23/04

Respectfully submitted,

Kenneth W. Bolvin

Kenneth W. Bolvin

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